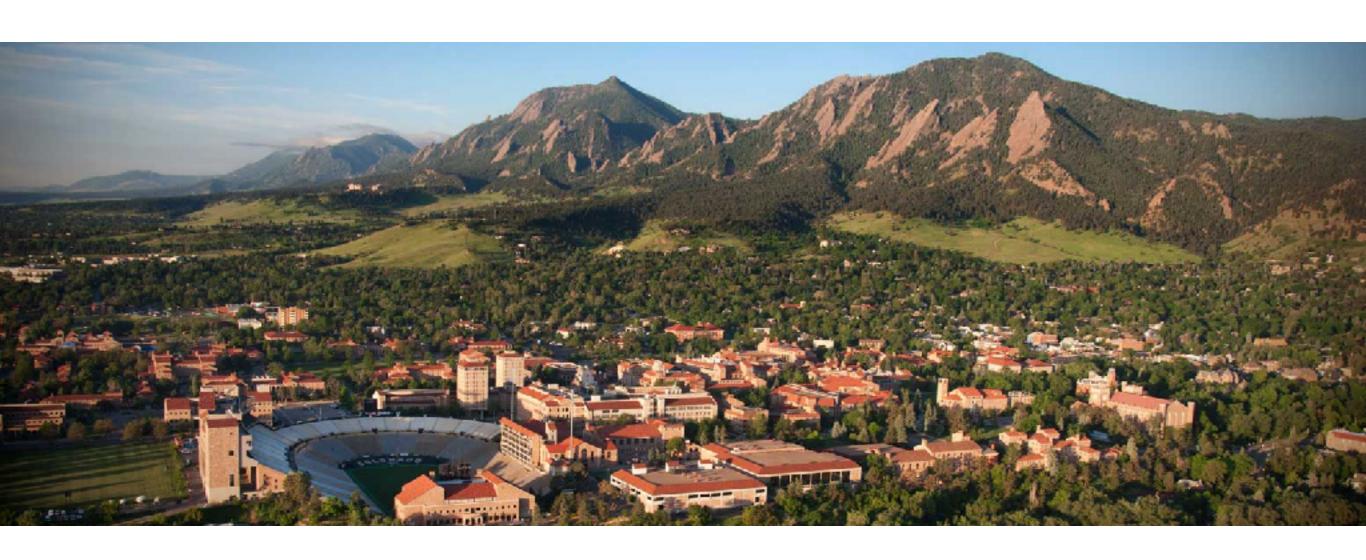
Packet-Level Network Analytics without Compromises

NANOG 73, June 26th 2018, Denver, CO

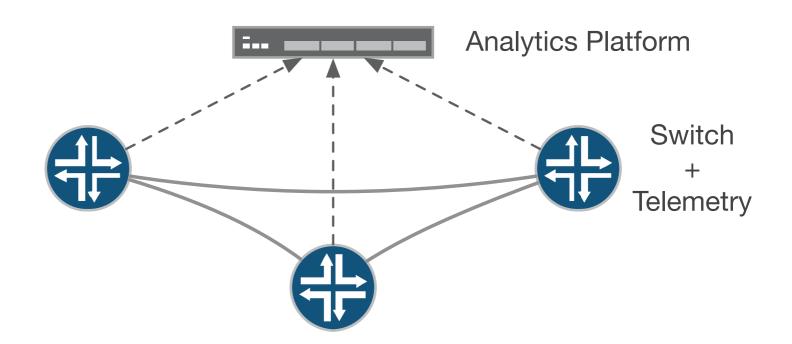


Oliver Michel



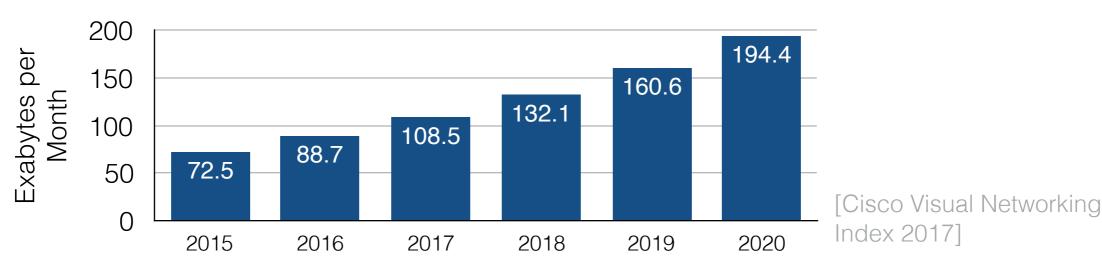
Network monitoring is important

- Security issues
- Performance issues
- Equipment failure
- Misconfiguration



Network traffic and security threats grow rapidly



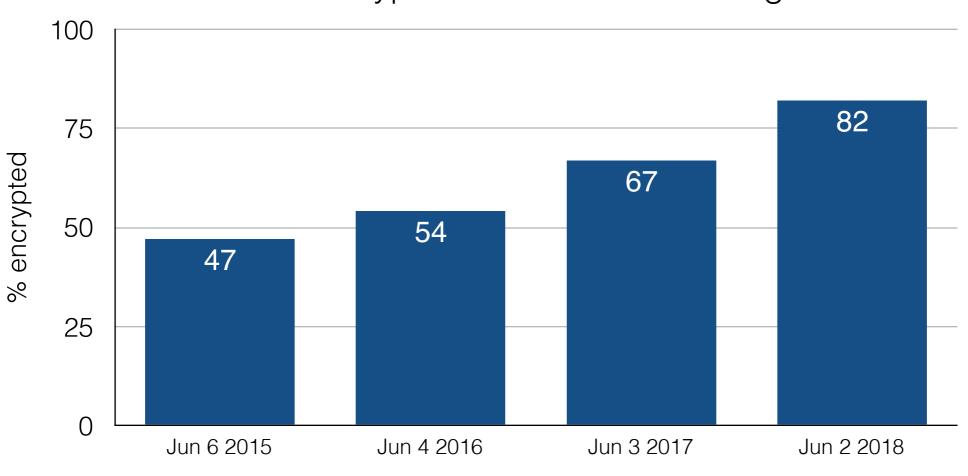


Total Ransomware Samples



Traffic is commonly encrypted





[Google Transparency Report 2018]

Network monitoring systems must match challenges

An ideal network monitoring system

record of every single packet

full programmability

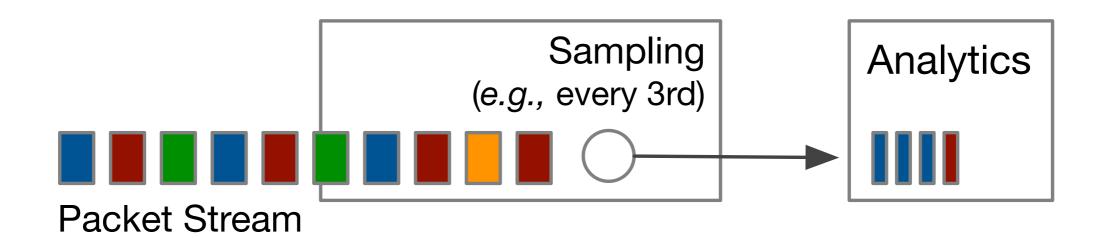
DC scale performance

Existing systems make compromises

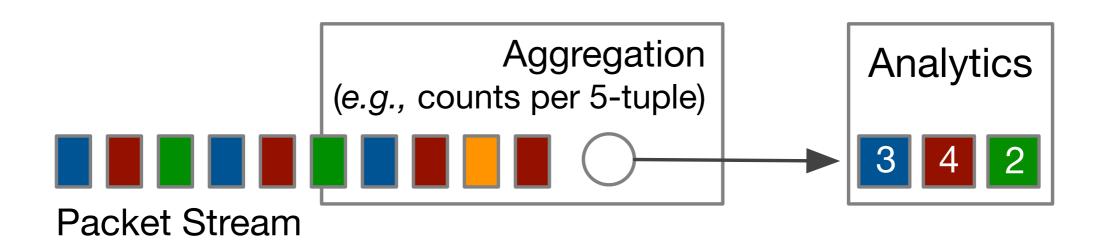
Filtering limits possible applications



Sampling can easily miss important packets



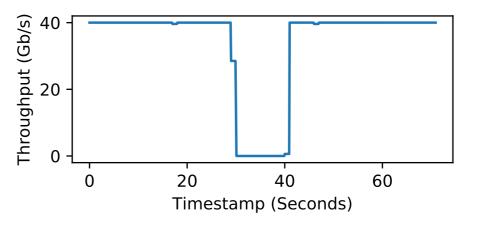
Aggregation limits information granularity and thus applications



Fixed hardware pipelines hinder expressiveness



Minimum downtime observed in 50 trials of reloading a Tofino PFE



Loss of information

Loss of capability

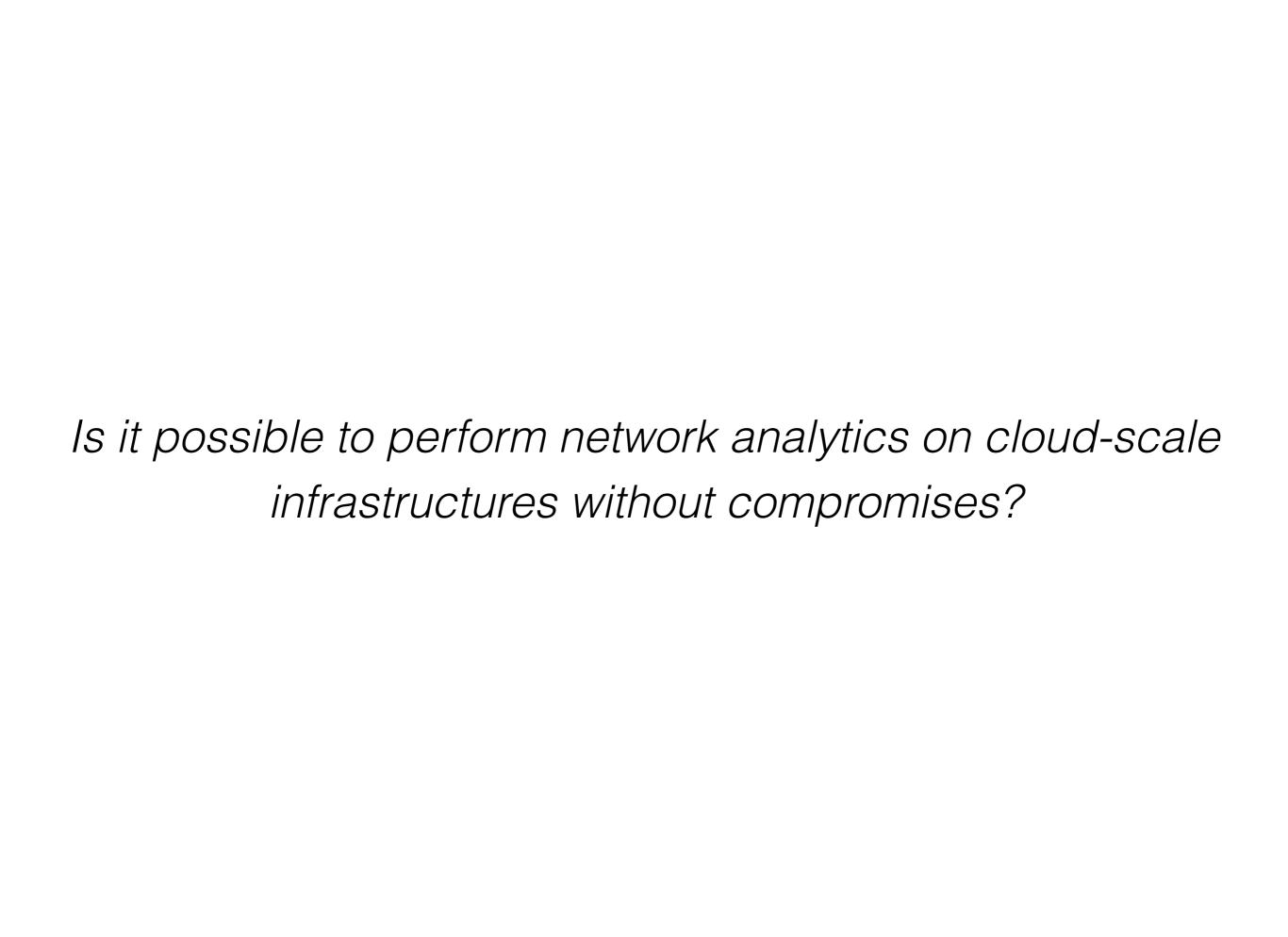
Why are these compromises made?

Case Study: Cisco Tetration for FB Data Center

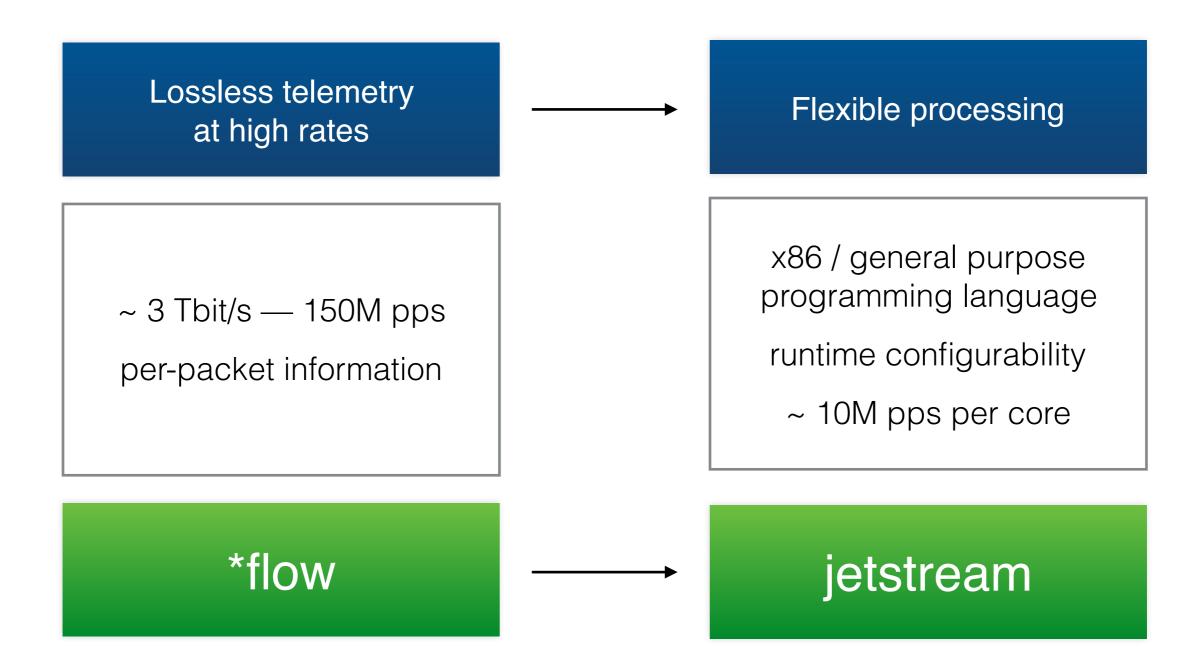
Cisco Tetration-V:

- up to 200K flow events/s
- per instance requirements for Tetration-V ESXi: 128
 CPU cores, 2TB RAM, 18TB storage
- 5 such servers for flow monitoring

Facebook web cluster (176 servers): 827K flows/s [roy. et. al. inside the social networks datacenter network 2015]



Two goals



Lossless telemetry at high rates

~ 3 Tbit/s — 150M pps per-packet information

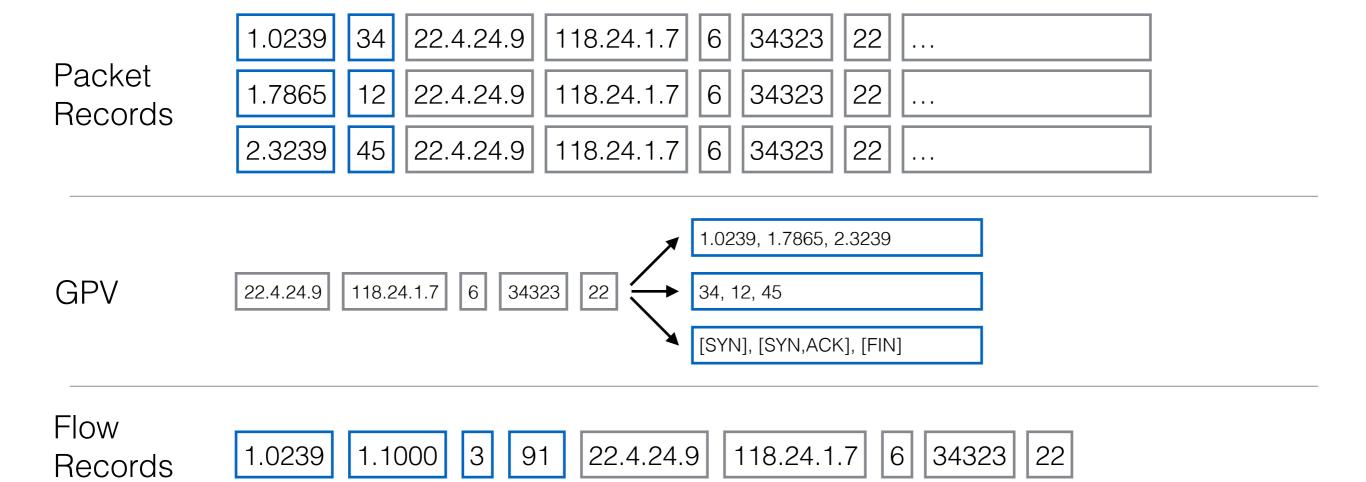
Record format

Hardware-assisted record generation

*flow

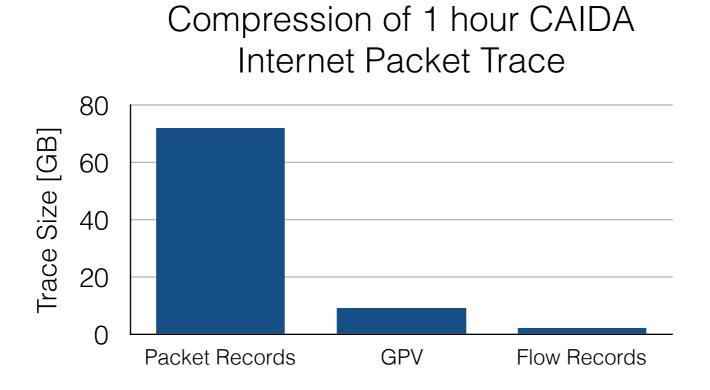
Grouped Packet Vectors (GPV)

- per-packet header fields
- meta data: e.g., queue depth, ingress/egress timestamps



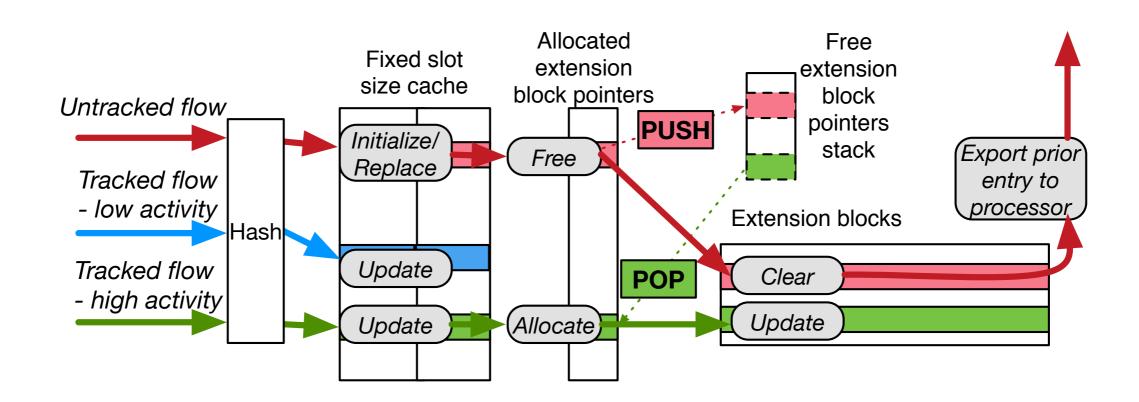
Grouped Packet Vectors (GPV)

GPVs provide high compression while maintaining information richness

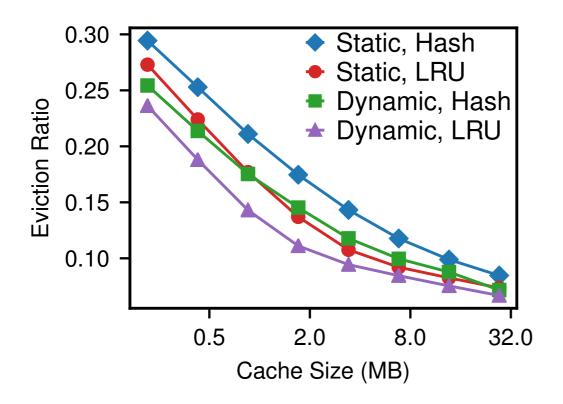


Generating GPVs at line rate

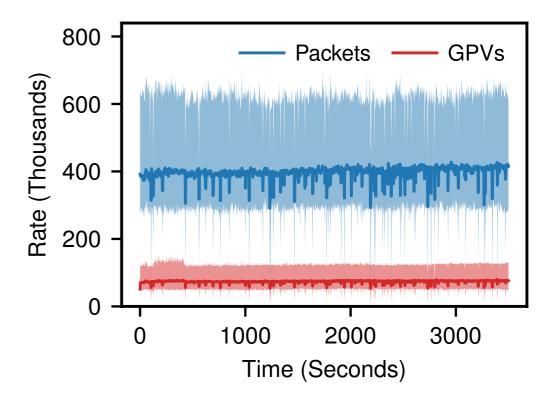
- Problem: GPVs have variable length, space is constrained
- Custom 2-level cache data structure
 - 1. Tall cache with narrow slots (many short flows)
 - 2. Small cache of wide slots (few long flows)



Resource usage



PFE memory vs. eviction rate



GPV eviction vs. packet rate

Scalability

- Optimizations for packet record workloads
- Programming API

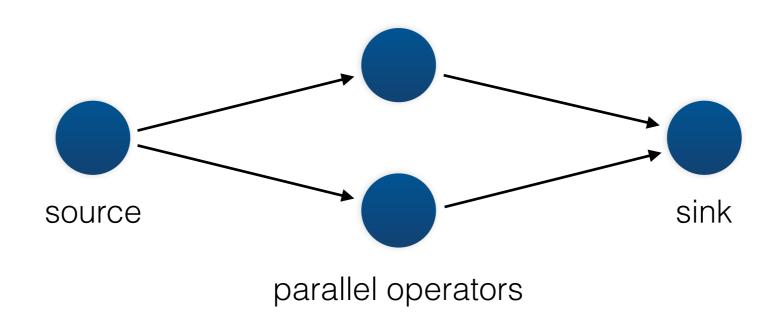
Flexible processing

x86 / general purpose programming language runtime configurability

~ 10M pps per core

jetstream

Leveraging parallel computation

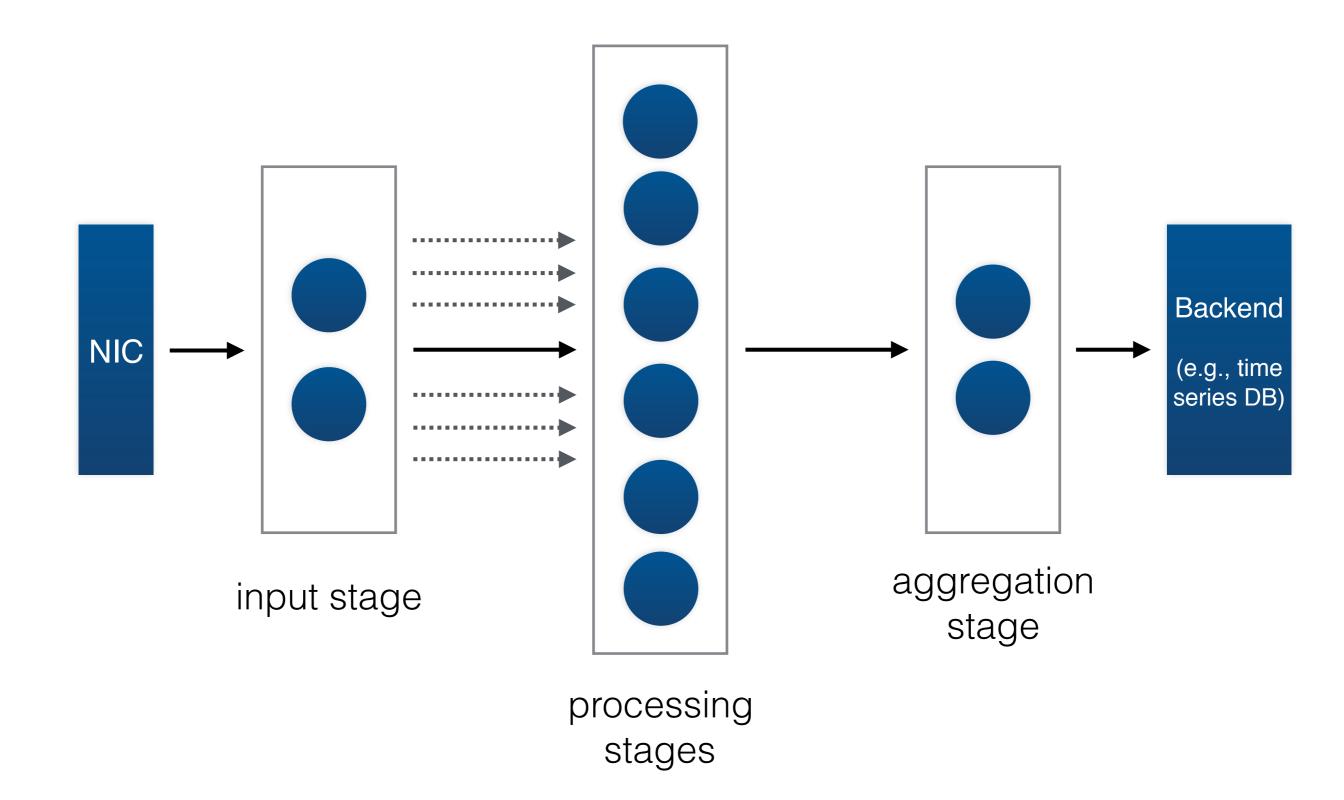








Jetstream architecture



Jetstream architecture

NUMA awareness pipeline 1→ CPU socket 1 Backend NIC (e.g., time series DB)

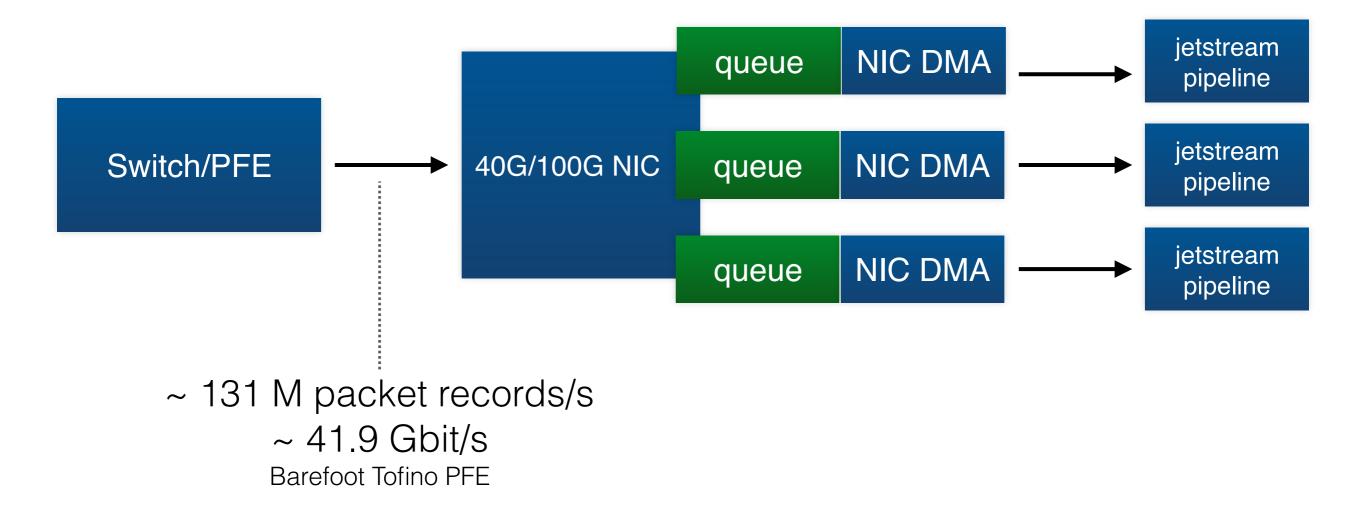
pipeline 2→ CPU socket 2

Characteristics of packet record workloads

Can we use properties of packet analytics workloads to our advantage?

- Network attached input
- Partitionability
- Small, simple, well-formed records
- Aggregation

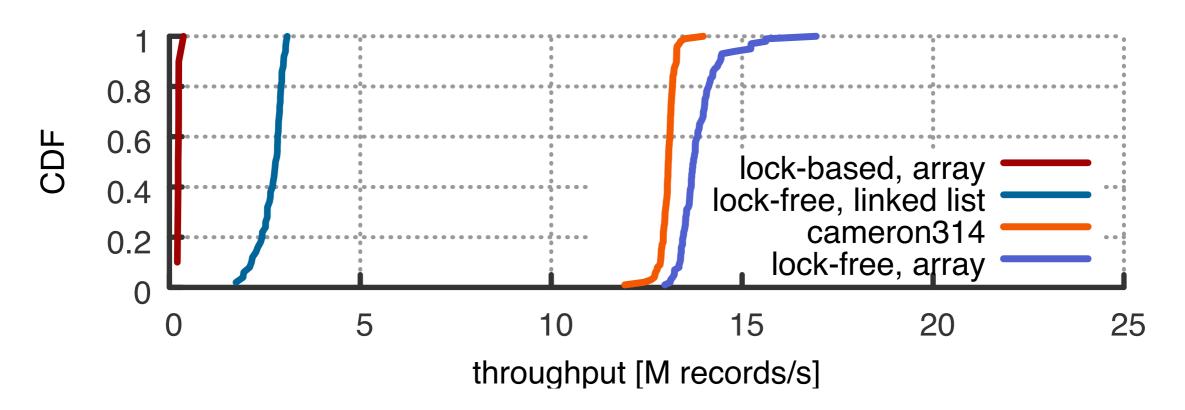
Network attached input



Many small records

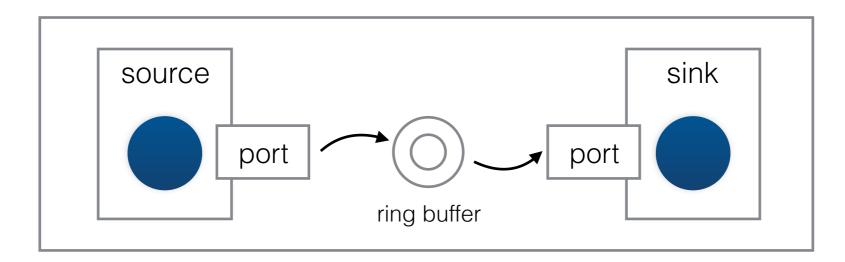
- Array vs. linked list
- Lock-free design
- Wait-free design
- Zero-copy operations

```
1 bool enqueue(const T& element_)
2
3 while (!q.enqueue(e)) { }
4
5 if (!q.enqueue(e))
6 std::this_thread::yield();
```



Programming abstraction

Application definition



```
int main(int argc, char** argv)

{
    jetstream::app app;

    auto source = app.add_stage<source>(1, "enp6s0f0");

    auto sink = app.add_stage<sink>(1, std::cout);

    app.connect<jetstream::pkt_t>(source, sink);

    app();

    return 0;

}
```

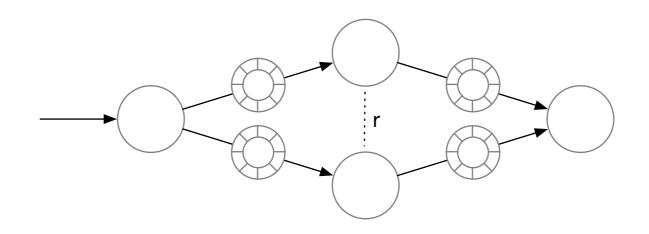
Programming abstraction

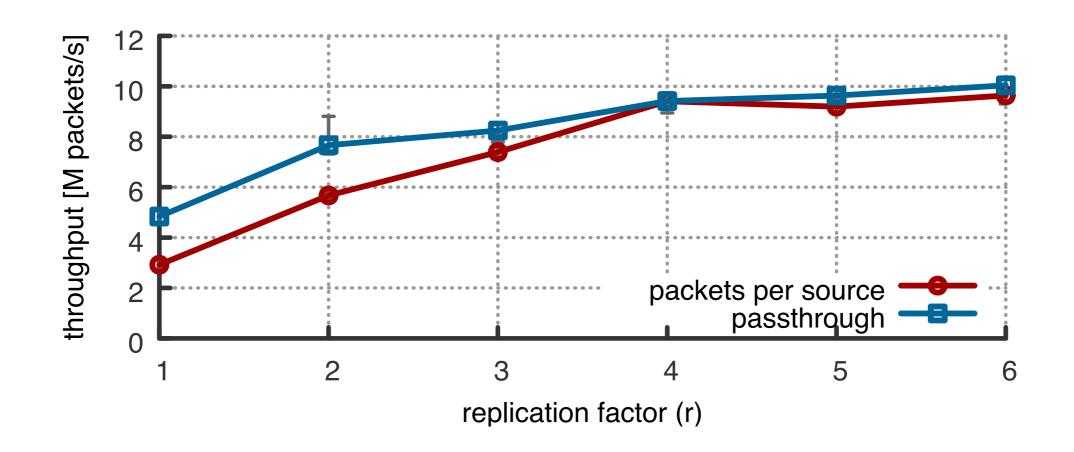
Processor definition

```
1 class source : public jetstream::proc {
2   [...]
3 };
```

```
1 explicit source(const std::string& iface_name_) : proc() {
2    add_out_port<jetstream::pkt_t>(0);
3    [...]
4 }
```

Performance

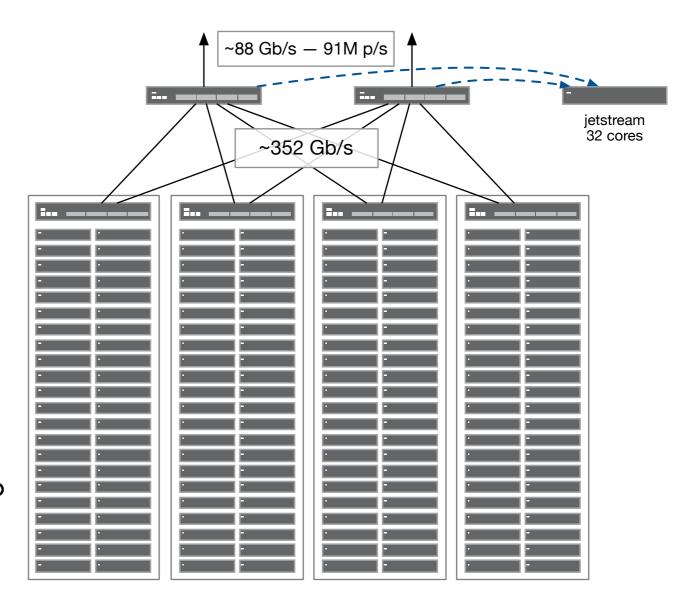




Evaluation

Facebook cluster study

- 2.9M packets/core: 32/64 cores for 4/8 racks
- StreamBox: 5096/10192 cores (163x)



[Arjun Roy, Hongyi Zeng, Jasmeet Bagga, George Porter, and Alex C. Snoeren. 2015. Inside the Social Network's (Datacenter) Network. SIGCOMM Comput. Commun. Rev. 45, 4 (August 2015), 123-137].

Conclusion

jetstream — high-performance, software network analytics platform

Conclusion

John Sonchack, Oliver Michel, Adam J. Aviv, Eric Keller, Jonathan M. Smith

Scaling Hardware Accelerated Monitoring to Concurrent and Dynamic Queries with *Flow

To appear: USENIX ATC 2018

Scaling Hardware Accelerated Monitoring to Concurrent and Dynamic Queries With *Flow

Iohn Sonchack*, Oliver Michel[†], Adam I. Aviv[†], Eric Keller[†], and Jonathan M. Smith*

*University of Pennsylvania, [‡]United States Naval Academy, and [‡]University of Colorado, Boulder

Abstract

We introduce *Flow, a practical system for hardware accelerated traffic monitoring. *Flow is highly scalable and able to execute many concurrent and dynamically changing traffic queries with minimal network discupion. The design insight is to move query specific computation off of the switch ASIC and into software running on commodity servers. We evaluated *Flow on a 3.2 Th/s Barefoot Tofino switch on-which we developed a novel dynamic cache data structure to build and export to software flow records that contain per-packet information in a compact, disaggregated format that enables highly efficient software processing. We demonstrate *Flow's capability to efficiently support multiple concurrent queries at scale through a Ratitib stream pro-

and network resources required for the monitoring infrastructure [39]. There are two other important requirements that the compiled query model does not address: concurrency and dynamic queries.

First, support for concurrent traffic queries. In most nativerles, there are often multiple applications or operators observing the network concurrently but with different queries. A practical monitoring infrastructure needs to multiplex the PFE across all the concurrently active queries. This is a challenge when the entire query is compiled to the PFE. Each query requires different computation that, given the line-rate processing model of a PFE [49], must map to dedicated computational resources, which are limited in PFEs.

Equally important for practical deployment is support

Oliver Michel, John Sonchack, Eric Keller, Jonathan M. Smith

Packet-Level Analytics in Software without Compromises

To appear: USENIX HotCloud 2018

Packet-Level Analytics in Software without Compromises

Oliver Michel University of Colorado Boulder Eric Keller

University of Colorado Boulder

John Sonchack University of Pennsylvania Jonathan M. Smith

University of Pennsylvania

Abstract

Traditionally, network monitoring and analytics systems ruly on aggregation (e.g., flow records) or sampling to cope with high packet rates. This has the downside that, in doing so, we lose data granularity and accuracy, and in general limit the possible network analytics we can perform. Recent proposals leveraging software-defined networking or programmable hardware provide more finegrained, per-packet monitoring but still are based on the fundamental principle of data reduction in the network, before analytics. In this paper, we provide a first step towards a cloud-scale, packet-level monitoring and analytics system based on stream processing entirely in software. Software provides virtually unlimited programmability and makes modern (e.g., machine-learning) net-

just couldn't process the information fast enough. These approaches, of course, reduce information – aggregation reduces the haid of the analytics system at the cost of granularity, as per-packet data is reduced to groups of packets in the form of soms or counts [3, 15]. Sampling and filtering reduces the number of packets or flows to be analyzed. Reducing information reduces load, but it also increases the chance of missing critical information, and restricts the set of possible applications [30, 28].

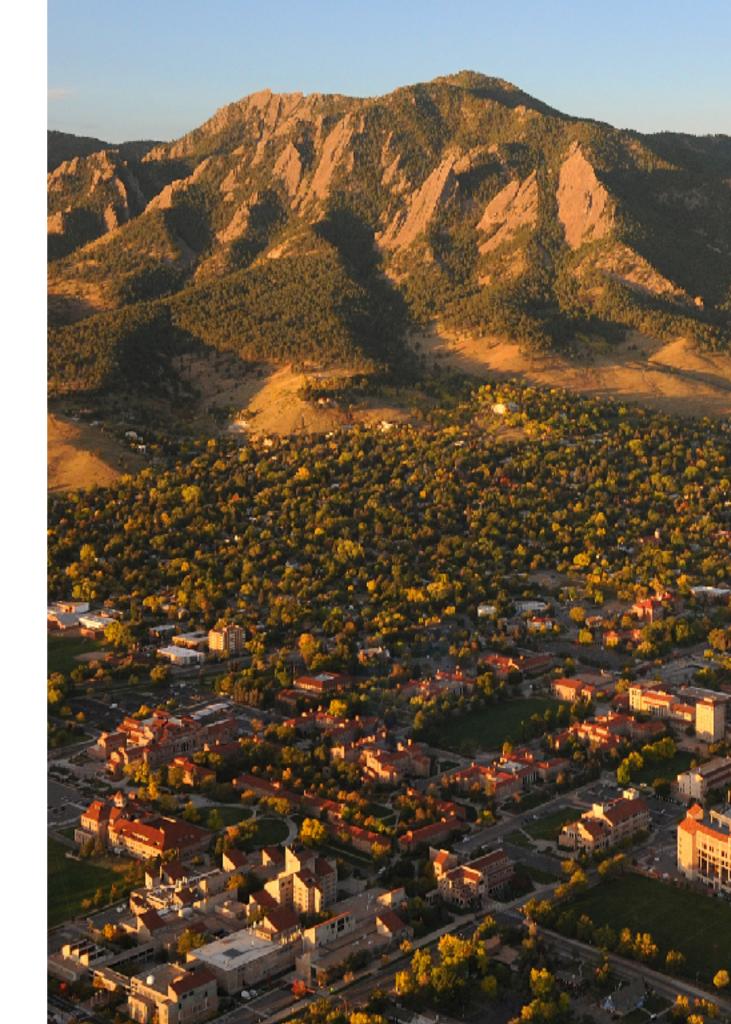
Recent advances in software-defined networking (SDN) and more programmable hardware have provided opportunities for more fine-grained monitoring, towards packet-level network analytics. Packet-level analytics systems provide the benefit of complete insight into the network and open up opportunities for applications that require propagate data in the network [37]. But propagate

Q&A / DISCUSSION

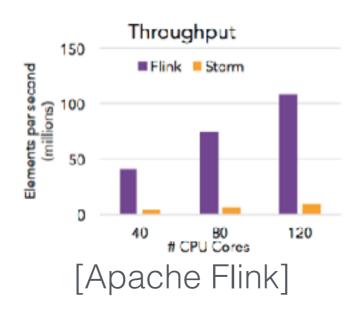
Oliver Michel

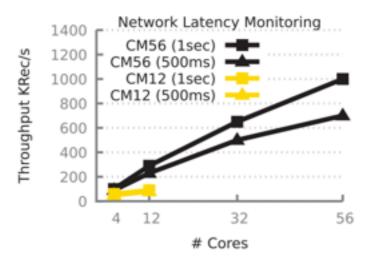
oliver.michel@colorado.edu http://nsr.colorado.edu/oliver





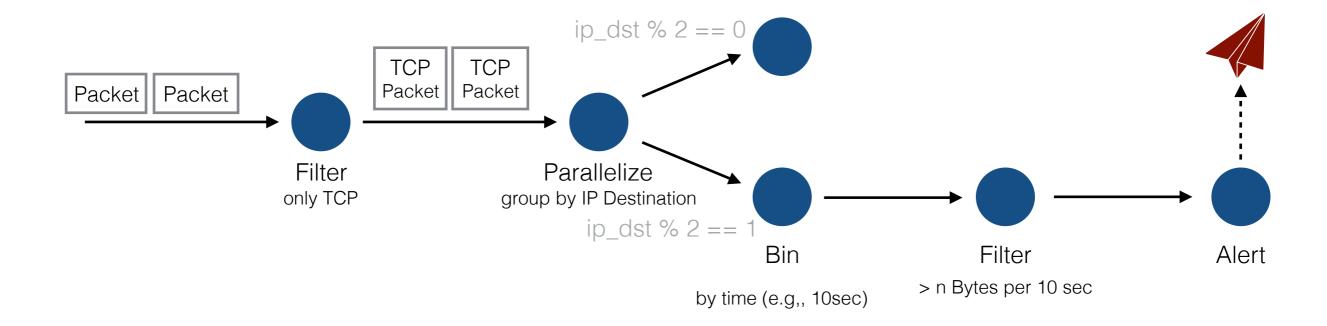
BACKUP SLIDES



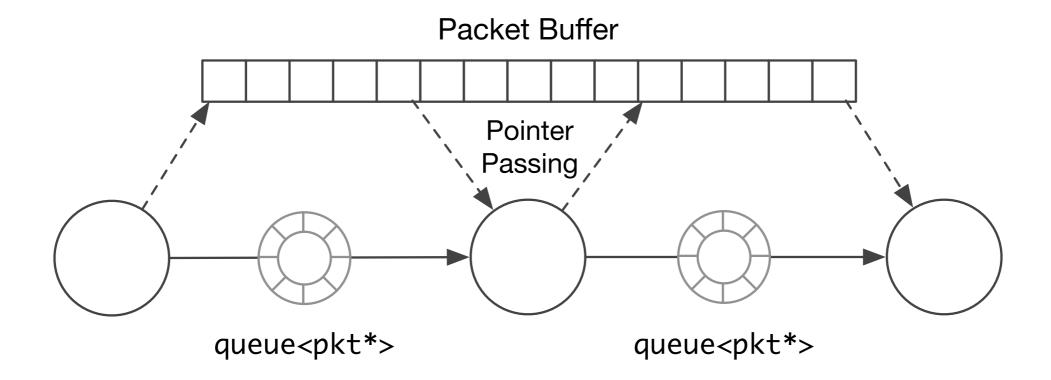


[StreamBox Miao '18]

Stream Processing

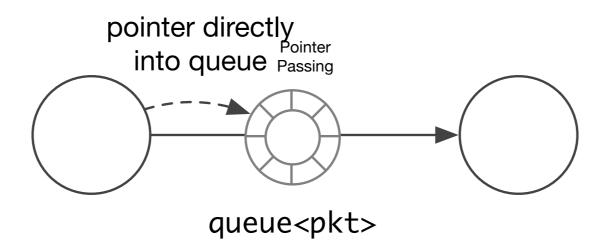


Reducing copy operations



Reducing copy operations

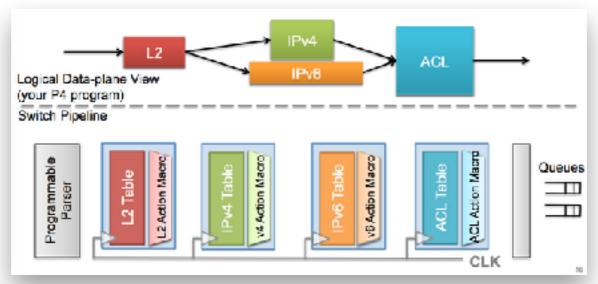
```
1 packet p;
2 p.ip_proto = 6;
3 q.enqueue(p);
```



```
1 auto p = q.enqueue();
2 p->ip_proto = 6;
```

Technologies

- Programmable switches and PISA: Protocol Independent Switch Architecture
 - Reconfigurable match-action tables in hardware
 - multiple stages with TCAM/ALU pair, fixed processing time, guarantees line rate





P4: Programming Protocol-Independent Packet Processors

Pat Bosshart, Dan Daly', Glen Gibb', Martin Izzard', Nick McKeown', Jennifer Rexford'', Cole Schlesinger''. Dan Talayco'. Amin Vahdat'. George Varghese'. David Walker'' 'Barefoot Networks 'Intel 'Stanford University ''Princeton University 'Google 'Microsoft Research

ABSTRACT

P4 is a high-level language for programming protocol-independent packet processors. P4 works in conjunction with SDN costrol protocols like OpenFlox. In its current form, OpenFlow explicitly specifies protocol headers on which it operates. This set has grown from 12 to 41 fields in a few years, increasing the complexity of the specification while still not providing the featblifty to add new banders. In this

multiple stages of rule tables, to allow switches to expose more of their capabilities to the controller.

The proliferation of new header fields shows no signs of stopping. For example, data-center network operators incassingly want to apply new forms of packet encapsulation (e.g., NVGRE, VXLAN, and STT), for which they retor deploying coffware switches that are easier to extend with new functionality. Rather than repeatedly extending